

EAST

File View Edit Tools Window Help

Go Back Forward Stop Print

leila.wap.1

- Drafts
- BRS:
- Pending
- Active
 - L1: (60402) phase near2 generat\$3
 - L2: (200140) phase\$1 near2 (signal\$1 or generat\$3)
 - L3: (217443) (burst or phase\$1) near2 (signal\$1 or generat\$3)
 - L4: (4221) 3 adj5 (gate\$1 or fetch\$3)
 - L5: (692) conver\$3 adj2 burst\$1
 - L6: (50) 4 and 5
 - L7: (18) 4 adj5 (latch\$3 or conver\$3) and burst
 - L8: (47) 4 adj5 (latch\$3 or conver\$3 or buffer or flipflop or flip) and burst
 - L9: (24070) 2 near5 clock
 - L10: (2136) (latch\$3 or conver\$3) near5 (clock near2 (clocker phase))
 - L11: (1263) 9 and 10
 - L12: (665) 9 with 10
 - L13: (4518) (latch\$3 or conver\$3) near5 (clock near2 (clock or phase))
 - L14: (691) 9 with 13
 - L15: (3) 14 and 5
 - L16: (824) 9 same 13
 - L17: (4) 16 and 5
 - L18: (120) 16 and burst
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

1/6 and burst

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	3	
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20030133527 A1	20030717	25	Method and circuit for adjusting the timing of	375/354			Joo, Yangsung et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20030097535 A1	20030522	32	High speed memory system	711/169			Hsu, Fu-Chieh et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20030076899 A1	20030424	11	Polyphase channelization system	375/316			Kumar, Rajendra et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	<input type="checkbox"/>	<input type="checkbox"/>	US 20030020830 A1	20030130	13	video signal converter for converting non-interlaced to	348/447	348/446		Sani, Mehdi H. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	<input type="checkbox"/>	<input type="checkbox"/>	US 20030005250 A1	20030102	26	Method and system for adjusting the timing offset	711/167	711/104; 711/118		Johnson, Brian et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Ready

